

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1453	((monitor\$4 calibrat\$4 test\$4) near3 (tim\$4 phase delay) and (control\$4 computer cpu microprocessor mpu processor) and (driv\$4)).clm.	US-PGPUB	OR	OFF	2009/05/22 09:35
L3	223	((monitor\$4 calibrat\$4 test\$4) near3 (tim\$4 phase delay) same (control\$4 computer cpu microprocessor mpu processor) near10 (driv\$4)).clm.	US-PGPUB	OR	OFF	2009/05/22 09:40
L4	9	3 and "369"/\$	US-PGPUB	OR	OFF	2009/05/22 09:41
S66	296	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) same (driver diode laser) and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:04
S67	0	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) same (driver diode laser) same (openloop open adj loop)and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:04
S68	0	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) same (driver diode laser) same (openloop open adj loop) and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:04
S69	12	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) same (driver diode laser) and (openloop open adj loop) and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:04

S70	69	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) same (driv\$4) near3 (source diode laser) and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:05
S71	1	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) near10 (abnormal\$4 defect\$4 error\$4) same (driv\$4) near3 (source diode laser) and 369/59\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:21
S72	7	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) near10 (abnormal\$4 defect\$4 error\$4) same (driv\$4) near3 (source diode laser) and "369"/\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:21
S73	10	(timing phase delay clock) near10 (monitor \$4 test\$4 determin\$4) near10 (abnormal\$4 defect\$4 error\$4) same (driv\$4) near3 (element servo acutator motor) and "369"/\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:47
S74	8	(timing phase delay clock) near10 (feedback loop) near10 (driv\$4) near3 (diode soruce light) and "369"/\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:57
S75	3	("5676600").PN. ("20010002864"   "7113434").PN.	US-PGPUB; USPAT	OR	OFF	2009/05/13 13:59
S76	262	(feedback loop) near6 (driv\$4) near7 (control \$4) same (timing phase delay) and "369"/\$	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:01

S77	0	(feedback loop) near6 (driv\$4) near7 (control \$4) same (timing phase delay) and "369"/59.11	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:02
S78	101	(feedback loop) near6 (driv\$4) near7 (control \$4) same (timing phase delay) and (369/59\$ 369/53\$)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:02
S79	9	("20020009026"   "20030053387"   "20030147315"   "5146443"   "5367513"   "6157601"   "6560173"   "6611123"   "6906986").PN. OR ("7298674").URFN.	US-PGPUB; USPAT; USOCR	OR	OFF	2009/05/13 14:08
S83	1046	(test\$4 monitor\$4) near5 (channel signal) near5 (timing phase delay) same (driver diode laser)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:38
S84	5	(test\$4 monitor\$4) near5 (channel signal) near5 (timing phase delay) near15 (driver diode laser) near10 (control cpu mpu) near4 (feedback loop)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:39
S85	257	(test\$4 monitor\$4) near5 (timing phase delay) near15 (driver diode laser) near10 (control cpu mpu)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:40
S86	155	(test\$4 monitor\$4) near5 (timing phase delay) near7 (driver diode laser) near10 (control cpu mpu)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:41
S87	80	S86 and (writ\$4 record \$4)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:41
S88	4	(self) near3 (test\$4 monitor\$4) near7 (driver diode laser) near10 (control cpu mpu) same (timing phase delay)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:43

S89	25	(self) near3 (test\$4 monitor\$4) near7 (driver diode laser) near10 (control cpu mpu)	US-PGPUB; USPAT	OR	OFF	2009/05/13 14:43
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